Animesh Chhotaray

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APPOINTMENTS

Research Scientist   2023-
Georgia Tech, USA

- Conduct research on cybersecurity of systems with focus on protecting digital artifacts in different supply chains (e.g., integrated-circuit, additive-manufacturing, cyber-physical systems).
- Mentor Masters and PhD students in the Cyber-Physical Security lab and Stebner labs.
- Write grant proposals.

Teaching Associate   2013-2015
KIIT University, Bhubaneswar, India

- Research: Develop image-encryption techniques using orthonormal and self-invertible matrices.
- Teaching: Programming in C, Computer Security.

Engineer   2011-2013
Samsung Research & Development Institute, NOIDA, India

- Worked for Systems team (FM driver) on feature phones with ARM based SoC ranging from 2G (48xx, 49xx) to 3G (68xx).
- Worked on code optimization, bug fixing and code stabilization through analysisof RAM dumps and traces, and test cases performed for side effects.

EDUCATION

PhD Computer Science   2017-2023
University of Florida   GPA: 3.8/4.0

- **Dissertation Title**: Provable-Security Treatment Of Circuit-Design Intellectual-Property Theft In Integrated-Circuit Supply Chain
- Courses: Computer and Information Security, Mathematics for Intelligent Systems, Automated Software and Hardware Verification, Applied Machine Learning

MS Computer Science   2015-2017
University of Florida   GPA: 3.8/4.0

- Courses : Computer Network Security, Penetration Testing, Advanced Data Structures, Introduction to Modern Cryptography, Network Algorithms and Data Structures, Analysis of Algorithms, Programming Language Principles, Computer Architecture

BTech Computer Science   2007-2011
NIT Rourkela, India   GPA: 8.13/10

- Courses : C, C++, Data Structures, Operating Systems, Computer Networks, Computer Organization and Architecture, Theory of Computing, Algorithms

PUBLICATIONS

4. R. Pickren, A. Chhotaray, F. Li, S. Zonouz, R. Beyah, "Release the Hounds! Automated Inference and Empirical Security Evaluation of Field-Deployed PLCs using Active Network Data", *ACM Conference on Computer and Communications Security (CCS)* , (2024).

3. A. Chhotaray, T. Shrimpton, "Hardening Circuit Design IP Against Reverse-Engineering Attacks", *IEEE Security & Privacy* , (2022).

2. W. Garcia, A. Chhotaray, J. Choi, S. K. Adari, K. Butler, S. Jha, "Brittle Features of Device Authentication", *ACM CODASPY* , (2021).

1. A. Chhotaray, A. Nahiyan, T. Shrimpton, D. Forte, M. Tehranipoor, "Standardizing Bad Cryptographic Practice - A teardown of the P1735 IEEE standard for protecting electronic-design intellectual property", *ACM Conference on Computer & Communications Security (CCS)* , (2017).

AWARDS AND RECOGNITION

- CCS'17 paper resulted in 7 Common Vulnerabilities and Exposures (CVE) entries in the Vulnerability Notes Database

- CCS'17 paper featured in The Register, threatpost, The Hacker News, and other cybersecurity news publications

- Graduate Fellowship Award in 2019 and 2022

SERVICE

- PC member, Dependable Systems and Networks (DSN), 2025

- Judge of Graduate Poster Symposium, 2024, College of Computing, Georgia Tech

- External reviewer, Network and Distributed System Security (NDSS), 2020

- Sub-reviewer CRYPTO ACM WiSec, 2020

MENTEES OUTSIDE GEORGIA TECH

- Kollin Labowski (PhD candidate, University of Florida)

- Soumojit Biswas (BTech, KIIT University)

- Zhang Zitong (MS, University of Florida)

- Noopur R. Kalawatia (MS, University of Florida)

- Ashwath Venkataraman (MS, University of Florida)